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Abstract. Top-Down VLSI Design: From Architectures to Gate-Level Circuits and FPGAs represents a unique approach to learning digital design. Developed from more than 20 years teaching circuit design, Doctor Kaeslins approach follows the natural VLSI design flow and makes circuit design accessible for professionals with a background in systems engineering or digital signal processing.

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H. Kaeslin. Published 2008. Engineering. VLSI circuits are ubiquitous in the modern world, and designing them efficiently is becoming increasingly challenging with the development of ever smaller chips. This practically oriented textbook covers the important aspects of VLSI design using a top-down approach, reflecting the way digital circuits are actually designed.

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Top-Down Digital VLSI Design, From Architectures to Gate-Level Circuits and FPGAs. by Hubert Kaeslin. Morgan Kaufmann Publishers, Boston. ISBN 978-0-12-800730-3.

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"Kaeslin (ETH Zurich, Switzerland) has provided a fresh top-down approach that makes the subject simple to teach and easy to learn. . . Students, instructors and practicing engineers will find it very useful." C. Mi, Choice

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