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A = 1 and B = 1, B=1, \! B = 1, the latch state is Q = 1, Q=1, Q = 1, (e) Derive a circuit for the A B AB A B latch using four two-input NOR \text{NOR}\! NOR gates and two inverters. An A B AB\! A B latch operates as follows: If A = 0 A=0\! A = 0 and B = 0, B=0,\! B = 0, the latch state is Q = 0; Q=0;\!

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