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Top Design Tips to Keep Your Communications Connected" series, we explore what protecting high-speed interfaces looks like, including USB, HDMI, DisplayPort, and eSATA. Numerous communication circuits ...

Design Tips for Protecting High-Speed Interfaces
This project addresses a set of unique challenges in circuits and systems arising in UAV swarm communications: i) swarm array synchronization and backhaul, ii) distributed processing on the edge ...

Circuits and Systems Design for UAV Swarm Enabled Communications
Keysight Technologies, Inc. (NYSE: KEYS), a leading technology company that delivers advanced design and validation solutions to help accelerate innovation to connect and secure the world, announced ...

Keysight PathWave Software Selected by Menlo Micro to Reduce Design Cycle for New Radio Frequency Microelectromechanical Switch
Modern communications ... in designing a new circuit. The author begins with a review of the basics - the origin of resistance, capacitance, and inductance - then progresses to more advanced topics ...

Electromagnetics for High-Speed Analog and Digital Communication Circuits
Qorvo, Microchip, MaxLinear, and Cree Wolfspeed have gone wide, electrically wide-band that is, with newly released GaN-based RF products for 5G and satellite communications.

A Week of GaN: 3 MMICs Target Ka-band, Reduced Area, and 5G Base Stations
Diakopto announced today that IQ-Analog, a leading provider of wideband transceivers for 5G wireless systems, has selected ParagonX™ to accelerate the analysis, debugging and optimization of their ...

IQ-Analog Adopts Diakopto's ParagonX™ Platform for Next-Generation 5G Wireless Communications ICs
By providing onboard communications, high short-circuit current rating and compact design, the PowerXL DM1 micro drives are engineered to help customers reduce system costs and meet the challenges ...

VFD Simplifies Integration, Operation
In an interview with Power Electronics, Jeff DeAngelis, Vice President Industrial Communications at Maxim Integrated ... of the TMCM-1617-GRIP-REF reference design is its ability to integrate multiple ...

Reference design simplifies industrial robotic motor control
The main objective of the BTech (ECE) programme is to equip students with necessary core competency to succeed long-term in engineering/ entrepreneurship careers after completing their B.Tech.

What are the exams after ECE B.Tech
The required NFC communication performance can also be ensured by selecting suitable packaging materials and circuits at the stage of package design, which assumes the presence of the NFC tag.

Toppan Develops Smart Packages with Built-in NFC Functions
In this article In a unanimous vote on Tuesday, the Federal Communications Commission finalized a $ ... Shanghai High-Performance Integrated Circuit Design Center, Sunway Microelectronics, the ...

FCC finalizes program to rip and replace Huawei, ZTE telecom equipment in the U.S.
and mixed-signal integrated circuit design and provides custom turnkey ASICs for global customers across industries, including automotive, medical, industrial, consumer, and communications.

Cyient Celebrates First Anniversary of IC Design and Development Center in Duisburg, Germany
Controlling an electronic circuit remotely The case study of the ... which we achieved with the design of the laboratory and the experiments carried out," stressed the researcher.

A remote laboratory for performing experiments with real electronic and communications equipment
from analogue and digital circuit design, layout and verification to distributed applications and embedded software. LSI capacitates communication devices such as smartphones, audio equipment and ...

Sanei Hytechs' design expertise will boost Chinese semiconductor market
Keysight is already the industry leader in both the simulation and test of high-speed circuits. Together with Altium, we can complete the end-to-end high-speed PCB design workflow." Streamlining ...

Keysight Technologies Joins Altium's Nexar Partner Program
AnSem, a Cyient company, announced the first anniversary of the acquisition of an integrated circuit (IC) design center in Duisburg, Germany.

Diode Lasers and Photonic Integrated Circuits, Second Edition provides a comprehensive treatment of optical communication technology, its principles and theory, treating students as well as experienced engineers to an in-depth exploration of this field. Diode lasers are still of significant importance in the areas of optical communication, storage, and sensing. Using the the same well received theoretical foundations of the first edition, the Second Edition now introduces timely updates in the technology and in focus of the book. After 15 years of development in the field, this book will offer brand new and updated material on GaN-based and quantum-dot lasers, photonic IC technology, detectors, modulators and SOAs, DVDs and storage, eye diagrams and BER concepts, and DFB lasers. Appendices will also be expanded to include quantum-dot issues and more on the relation between spontaneous emission and gain.

The latest techniques for designing robust, high performance integrated circuits in nanoscale technologies Focusing on a new technological paradigm, this practical guide describes the interconnect-centric design methodologies that are now the major focus of nanoscale integrated circuits (ICs). High Performance Integrated Circuit Design begins by discussing the dominant role of on-chip interconnects and provides an overview of technology scaling. The book goes on to cover data signaling, power management, synchronization, and substrate-aware design. Specific design constraints and methodologies unique to each type of interconnect are addressed. This comprehensive volume also explains the design of specialized circuits such as tapered buffers and repeaters for data signaling, voltage regulators for power management, and phase-locked loops for synchronization. This is an invaluable resource for students, researchers, and engineers working in the area of high performance ICs. Coverage includes: Technology scaling Interconnect modeling and extraction Signal propagation and delay analysis Interconnect coupling noise Global signaling Power generation Power distribution networks CAD of power networks Techniques to reduce power supply noise Power dissipation Synchronization theory and tradeoffs Synchronous system characteristics On-chip clock generation and distribution Substrate noise in mixed-signal ICs Techniques to reduce substrate noise

The monograph begins with a systematic introduction of chaos and chaos synchronization, and then extends to the methodologies and technologies in secure communication system design and implementation. The author combines theoretical frameworks with empirical studies, making the book a pratical reference for both academics and industrial engineers.

Masters Theses in the Pure and Applied Sciences was first conceived, published, and dis seminated by the Center for Information and Numerical Data Analysis and Synthesis (CINDAS) * at Purdue University in 1957, starting its coverage of theses with the academic year 1955. Beginning with Volume 13, the printing and dissemination phases of the ac tivity were transferred to University Microfilms/Xerox of Ann Arbor, Michigan, with the thought that such an arrangement would be more beneficial to the academic and general scientific and technical community. After five years of this joint undertaking we had concluded that it was in the interest of all concerned if the printing and distribution of the volume were handled by an international publishing. house to assure improved service and broader dissemination. Hence, starting with Volume 18, Masters Theses in the Pure and Applied Sciences has been disseminated on a worldwide basis by Plenum Publishing Corporation of New York, and in the same year the coverage was broadened to include Canadian universities. All back issues can also be ordered from Plenum. We have reported in Volume 25 (thesis year 1980) a total of 10,308 theses titles from 27 Canadian and 214 United States universities. We are sure that this broader base for theses titles reported will greatly enhance the value of this important annual reference work. While Volume 25 reports theses submitted in 1980, on occasion, certain universities do report theses submitted in previous years but not reported at the time.

Cutting-edge techniques for ultra-wideband, low-noise amplifier design This pioneering resource presents alternatives for implementing power- and area-efficient integrated low-noise amplifiers for ultra-wideband communications. Design methodologies for distributed amplifiers, feedback amplifiers, inductor structures with reduced area, and inductorless techniques are discussed. Cowritten by international experts in industry and academia, this book addresses the state of the art in integrated circuit design in the context of emerging systems. Design of Low-Noise Amplifiers for Ultra-Wideband Communications covers: Ultra-wideband overview and system approach Distributed amplifiers Wideband low-noise amplifiers Feedback wideband low-noise amplifiers Inductorless techniques

Introduction to Circuit Analysis and Design takes the view that circuits have inputs and outputs, and that relations between inputs and outputs and the terminal characteristics of circuits at input and output ports are all-important in analysis and design. Two-port models, input resistance, output impedance, gain, loading effects, and frequency response are treated in more depth than is traditional. Due attention to these topics is essential preparation for design, provides useful preparation for subsequent courses in electronic devices and circuits, and eases the transition from circuits to systems.